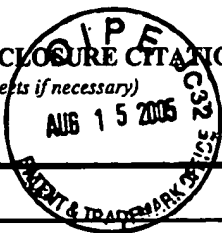


INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

YOR920030203US1 (16694)

Application Number

10/662,028

Applicant(s)

Stephen W. Bedell, et al.

Filing Date

September 12, 2003

Group Art Unit

3661 1742

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
GW		4,104,090	08/01/1978	Pogge, et al.			
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*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

GW		Mizuno, et al., "High Performance CMOS Operation of Strained-SOI MOSFETs Using Thin Film SiGe-On-Insulator Substrate", 2002 Symposium on VLSI Technology, Digest of Technical Papers, Honolulu, June 11-13, 2002, Symposium on VLSI Technology, New York, NY: IEEE, US, June 11, 2002, pp. 106-107, XP001109839
GW		Patent Abstracts of Japan, Vol. 005, No. 181 (E-083), November 20, 1981, 56-110247.

EXAMINER

GW

DATE CONSIDERED

8/19/05

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	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>On</i>		Patent Abstracts of Japan, Vol. 005, No. 117 (E-067), May 18, 1981, <i>56-056648</i> .

EXAMINER <i>Guy Uppert</i>	DATE CONSIDERED <i>8/19/05</i>
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.